L Number	Hits	Search Text	DB	Time stamp
-	19	(interface same (match matching)) and ((non$1volatile eeprom eprom) same comparator same (controller (control adj1 circuit)))	USPAT	2003/06/20 09:20
-	8	(serial adj1 bus) and ((non$1volatile eeprom eprom) same comparator same (controller (control adj1 circuit)))	USPAT	2003/06/18 13:46
-	277	(non$1volatile eeprom eprom) same comparator same (controller (control adj1 circuit))	USPAT	2003/06/18 12:20
-	136	interface and ((non$1volatile eeprom eprom) same comparator same (controller (control adj1 circuit)))	USPAT	2003/06/18 10:17
-	549	(flash eeprom eprom) same comparator same (controller (control adj1 circuit))	USPAT	2003/06/18 12:24
-	39	interface same ((flash eeprom eprom) same comparator same (controller (control adj1 circuit)))	USPAT	2003/06/18 12:10
-	5	(serial adj2 bus) and ((flash eeprom eprom) same comparator same (controller (control adj1 circuit)))	USPAT	2003/06/18 12:22
-	8355	(flash eeprom eprom) and comparator and (controller (control adj1 circuit))	USPAT	2003/06/18 12:24
-	476	(interface same (match matching)) and ((flash eeprom eprom) and comparator and (controller (control adj1 circuit)))	USPAT	2003/06/20 10:28
-	33415	(comparator comparison compar$3 match$3) near9 ((identification adj2 data) address$2 tag$1)	USPAT	2003/06/18 14:39
-	22078	((identification adj2 data) address$2 tag$1) near9 (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/18 16:33
-	1620	((comparator comparison compar$3 match$3) near9 ((identification adj2 data) address$2 tag$1)) same (((identification adj2 data) address$2 tag$1) near9 (non$1volatile flash rom eeprom eprom))	USPAT	2003/06/18 14:41
-	4	(serial adj2 bus) same (((comparator comparison compar$3 match$3) near9 ((identification adj2 data) address$2 tag$1)) same (((identification adj2 data) address$2 tag$1) near9 (non$1volatile flash rom eeprom eprom)))	USPAT	2003/06/18 14:42
-	421715	(serial adj2 bus) (controller (control adj1 circuit))	USPAT	2003/06/18 14:44
-	1117	(((comparator comparison compar$3 match$3) near9 ((identification adj2 data) address$2 tag$1)) same (((identification adj2 data) address$2 tag$1) near9 (non$1volatile flash rom eeprom eprom))) and ((serial adj2 bus) (controller (control adj1 circuit)))	USPAT	2003/06/18 14:44
-	287	(((comparator comparison compar$3 match$3) near9 ((identification adj2 data) address$2 tag$1)) same (((identification adj2 data) address$2 tag$1) near9 (non$1volatile flash rom eeprom eprom))) same ((serial adj2 bus) (controller (control adj1 circuit)))	USPAT	2003/06/19 11:06
-	1	5293424.pn.	USPAT	2003/06/18 16:31
-	0	5293424.pn	USPAT	2003/06/18 16:30
-	1	((voltage adj3 detector) same (predetermined adj2 state)) same (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/19 09:57
-	4	((voltage adj3 detector) same (predetermined adj2 state)) and (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/18 16:35
-	4	((voltage adj3 detector) same (predetermined adj2 state)) and (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/19 13:57
-	69146	(reset near3 (signal input))	USPAT	2003/06/19 13:53
-	54008	(reset adj2 (signal input))	USPAT	2003/06/19 13:44
-	262846	non$1volatile flash rom eeprom eprom	USPAT	2003/06/19 13:44
-	3051	((reset adj2 (signal input))) same (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/19 13:45
-	291	((reset adj2 (signal input))) near4 (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/19 13:47
-	0	(((reset adj2 (signal input))) near4 (non$1volatile flash rom eeprom eprom)) near10 advantag$8	USPAT	2003/06/19 13:48
-	632	((reset adj2 (signal input))) near10 (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/19 13:47
-	1	(((reset adj2 (signal input))) near10 (non$1volatile flash rom eeprom eprom)) near10 advantag$8	USPAT	2003/06/19 13:47
-	75	(((reset adj2 (signal input))) same (non$1volatile flash rom eeprom eprom)) same advantag$8	USPAT	2003/06/19 13:48
-	2360	(reset near3 (signal input)) and (predetermined adj2 state)	USPAT	2003/06/19 13:55
-	901	(non$1volatile flash rom eeprom eprom) and ((reset near3 (signal input)) and (predetermined adj2 state))	USPAT	2003/06/19 13:55
-	474	(reset near3 (signal input)) same (predetermined adj2 state)	USPAT	2003/06/19 13:56

-	23	(non$1volatile flash rom eeprom eprom) same ((reset near3 (signal input)) same (predetermined adj2 state))	USPAT	2003/06/19 14:19
-	551	(predetermined adj3 (input signal)) same (predetermined adj2 state)	USPAT	2003/06/19 13:59
-	1	((non$1volatile flash rom eeprom eprom) same ((reset near3 (signal input)) same (predetermined adj2 state))) and ((predetermined adj3 (input signal)) same (predetermined adj2 state))	USPAT	2003/06/19 13:59
-	58	(bonding near5 (wire pad)) near5 (non$1volatile flash rom eeprom eprom)	USPAT	2003/06/19 16:42
-	845	(semiconductor adj2 chip) near10 (interface i/f cpu)	USPAT	2003/06/19 16:44
-	9	(semiconductor adj2 chip) near10 ((interface i/f) same cpu)	USPAT	2003/06/19 16:45
-	1775	(controller (control adj1 circuit) interface i/f compar$3 match$3) same ((multi$1chip multi-chip (multi adj1 chip)) mcp)	USPAT	2003/06/20 10:09
-	12	(identification (predetermined adj2 state)) same ((controller (control adj1 circuit) interface i/f compar$3 match$3) same ((multi$1chip multi-chip (multi adj1 chip)) mcp)	USPAT	2003/06/20 09:24
-	7201	(multi$1chip multi-chip (multi adj1 chip))	USPAT	2003/06/20 09:27
-	1440	(controller (control adj1 circuit) and (interface i/f) and (compar$3 match$3)) and ((multi$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 09:29
-	1345	(controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) and ((multi$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 09:30
-	510	(controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) same (predetermined adj2 state)	USPAT	2003/06/20 09:38
-	1	((controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) same (predetermined adj2 state)) same identification	USPAT	2003/06/20 09:32
-	121	((controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) same (predetermined adj2 state)) and identification	USPAT	2003/06/20 09:37
-	1	(((controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) same (predetermined adj2 state)) and identification) and ((multi$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 09:32
-	2614	(controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) and (predetermined adj2 state)	USPAT	2003/06/20 10:10
-	1	((controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) and (predetermined adj2 state)) and identification and ((multi$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 09:38
-	4	((controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) same (predetermined adj2 state)) and ((multi$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 09:40
-	14	(predetermined adj1 state) and ((multi$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 09:40
-	15	((controller (control adj1 circuit) same (interface i/f) same (compar$3 match$3)) and (predetermined adj2 state)) and ((multi$1chip multi-chip (multi adj1 chip)))	USPAT	2003/06/20 10:10
-	8	(serial adj1 bus) and ((non$1volatile eeprom eprom) same comparator same (controller (control adj1 circuit)))	USPAT	2003/06/20 10:26
-	476	(interface same (match matching)) and ((flash eeprom eprom) and comparator and (controller (control adj1 circuit)))	USPAT	2003/06/20 11:33